

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S103	260	fingerprint and fpga and (oscillation manufacture)	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:12
S105	978	380/44.ccls.	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:14
S106	1735	(fingerprint key ) and fpga and (oscillation manufacture) and configur\$4	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:16
S107	255	(fingerprint key ) with configur\$4 and fpga and (oscillation manufacture)	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:16
S108	214	(fingerprint key ) with configur\$4 and fpga and (oscillat\$3 pulse)	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:23
S109	141	S107 and S108	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:23
S110	69	(fingerprint key ) with configur\$4 and fpga and (oscillat\$3 pulse) same (fingerprint key )	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:27

S111	589	713/194.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:34
S112	632	380/37.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:34
S113	1907	326/41.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:34
S114	519	380/255.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:34
S116	410	713/161.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:35
S117	3116	((fingerprint key ) and fpga and (oscillat\$3 pulse) and (fingerprint key ) )	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:35
S118	4	((fingerprint key ) and fpga and (oscillat\$3 pulse) and (fingerprint key ) ).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/11 19:36
S119	3	((fingerprint key ) and fpga and (oscillat\$3 pulse) and (fingerprint key ) ).clm.	US-PGPUB	OR	ON	2008/02/11 19:36
S120	4	((fingerprint key ) and (fpga pld circuit) and (oscillat\$3 pulse) ).clm.	US-PGPUB	OR	ON	2008/02/11 19:37
S121	4	S113 and S117	US-PGPUB	OR	ON	2008/02/11 19:37

S124	855	(computing computed compute generat\$3 creat\$3 deriv\$3) near3 (key fingerprint) with (voltage transistor fluctuation variation temperature frequency)	US-PGPUB	OR	ON	2008/02/12 10:58
S125	2	(computing computed compute generat\$3 creat\$3 deriv\$3) near3 (key fingerprint) with (voltage transistor fluctuation variation temperature frequency) same manufacture	US-PGPUB	OR	ON	2008/02/12 10:58
S126	37	(computing computed compute generat\$3 creat\$3 deriv\$3) near3 (key fingerprint) with (voltage transistor fluctuation variation temperature frequency) same manufact\$3	US-PGPUB	OR	ON	2008/02/12 10:58
S127	2	(computing computed compute generat\$3 creat\$3 deriv\$3) near3 (key fingerprint) with (voltage transistor fluctuation variation temperature frequency) same manufact\$3 and (clb fpga pld)	US-PGPUB	OR	ON	2008/02/12 10:59
S128	64	(computing computed compute generat\$3 creat\$3 deriv\$3) near3 (key fingerprint) with (voltage transistor fluctuation variation temperature frequency) and (clb fpga pld)	US-PGPUB	OR	ON	2008/02/12 11:00
S129	98	(computing computed compute generat\$3 creat\$3 deriv\$3) near3 (key fingerprint) with (voltage transistor fluctuation variation temperature frequency) and (clb fpga pld)	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/12 11:00

S130	12	((computing computed compute generat\$3 creat\$3 deriv\$3 calculat \$3) with (key fingerprint) with (voltage transistor fluctuation variation temperature frequency manufactur\$3 oscillat\$3 pulse clock) and ((clb fpga pld) (programmable near2 (logic gate))).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/12 11:18
S131	7	((computing computed compute generat\$3 creat\$3 deriv\$3 calculat \$3) with (key fingerprint) with (voltage transistor fluctuation variation temperature frequency manufactur\$3 oscillat\$3 pulse clock) and ((clb fpga pld) (programmable near2 (logic gate))).clm.	US-PGPUB	OR	ON	2008/02/12 11:19
S132	2132	713/193.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/12 11:53
S133	1017	713/193.ccls.	US-PGPUB	OR	ON	2008/02/12 11:53
S134	6	S124 and S133	US-PGPUB	OR	ON	2008/02/12 11:53
S135	147	((STEPHEN) near2 (TRIMBERGER).INV.	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/12 12:19

2/12/2008 1:58:43 PM

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